

# PATENT ABSTRACTS OF JAPAN

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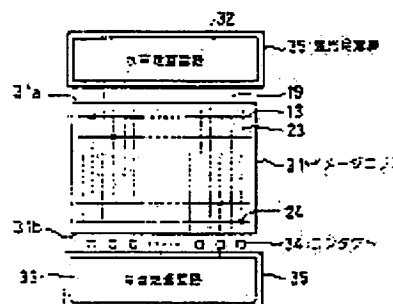
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## (54) SOLID-STATE IMAGE PICKUP DEVICE

### (57)Abstract:

**PURPOSE:** To reduce the chip area by forming a horizontal scanning circuit and a vertical scanning circuit along one and the same side of an image area respectively.

**CONSTITUTION:** A horizontal scanning circuit 32 and a vertical scanning circuit 33 are arranged along sides 31a, 31b of an image area 31 opposite to each other, the circuit 32 connects to a source line 19 and the circuit 33 connects to a gate line selection line 23 via a contact 34. Then a thin film 35 to shield the light is provided to the scanning circuits 32, 33. When the wiring in the circuit 33 is formed by using a 2-layer wiring made of metallic thin films of 1st and 2nd layers, the contact 34 connects an output wire for the circuit 33 of the 2nd layer metallic thin film and the line 23 of the 3rd layer metallic thin film. Thus, the longitudinal and lateral size of the solid-state image pickup element is reduced by the width of the scanning circuits and the chip area is reduced.



## LEGAL STATUS

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